IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: Unassigned Examiner: Unassigned

> INFORMATION **DISCLOSURE**

STATEMENT

n Re PATENT APPLICATION Of:

Applicant(s): Nobuhisa KUMAMOTO et al.

10/767.43**9** Serial No:

January 30, 2004 Filed:

SEMICONDUCTOR CHIP AND For:

PRODUCTION PROCESS

THEREFOR

Docket No.: Al 318D1

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

APR' 2 7 2005

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b)(3), i.e., prior to a First Office Action on the Merits.

Attached are copies of two Japanese patent publications, two Japanese utility model publications and a European patent publication. The publications were cited in the attached Japanese Office Action, dated February 8, 2005, for the corresponding Japanese Application 11-040399, and an English-language translation. The European application corresponds to one of the Japanese patent publications. A computer-generated English translation and an English-language abstract of the other Japanese patent publication also are provided. Any relevance of the Japanese publications can be gleaned from the Office Action. publications are listed on the attached Form PTO-1449.

Since this Information Disclosure Statement is being filed before the first Office Action, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to all documents submitted. Consideration of the submitted documents is respectfully requested.

Respectfully submitted,

April 27, 2005

Steven M. Rabin (Reg. No. 29.102) Date

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INFORMATION DISCLOSURE STATEMENT APR 2 7 7005				Atty Docket Al 318D1		Application	Application No.		
						10/767,439			
				Applicant Nobuhisa KUMAMOTO et al.					
				Filing Date January 30, 2004		Group Unit Unassigned			
	TRI	OFF		U.S.	PATENT DOCUMENTS				
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